FORM PTO-1449 (REV.7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 500118.02	APPLICATION NO. 10/633,247 ~~Not Yet Assigned~~
INFORMATION DISCLOSURE STATEMENT *(Use several sheets if necessary)*		APPLICANT(S) Theodore T. Pekny	
		FILING DATE 8/1/03 ~~Concurrently herewith~~	GROUP ART UNIT 2818 ~~Not Yet Assigned~~

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
TP	AA	5,087,834	02/11/92	Tsay	307	443	
TP	AB	5,280,455	01/18/94	Kanaishi	365	229	
TP	AC	5,712,590	01/27/98	Dries et al.	327	539	
TP	AD	5,862,096	01/19/99	Yasuda et al.	365	229	
TP	AE	6,101,137	08/08/00	Roh	365	194	
TP	AF	6,400,213 B2	06/04/02	Shih et al.	327	540	
TP	AG	6,492,863 B2	12/10/02	Kono et al.	327	538	
	AH						
	AI						
	AJ						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AK							
	AL							
	AM							

OTHER PRIOR ART *(Including Author, Title, Date, Pertinent Pages, Etc.)*

	AN	
	AO	
	AP	

EXAMINER Trong Phan	DATE CONSIDERED 2/3/04

* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).

FORM PTO-1449 (REV.7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 50118.02	APPLICATION NO. 10/633,247
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)		APPLICANT(S) Theodore T. Pekny	
		FILING DATE August 1, 2003	GROUP ART UNIT ~~2816~~ 28/8

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
TP	AA	3,887,863	06/03/75	Brokaw	323	19	
TP	AB	5,051,686	09/24/91	Schaffer	323	313	
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO
	AI							

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

TP	AJ	"Bandgap Circuit," (January 4, 2002) <http://www.ele.uri.edu/Courses/ele535/startup.kills/gap.html> pp. 1-2.
TP	AK	"Chopping: A Technique for Noise and Offset Reduction," Internet Business Systems, Inc., 2001, pp. 1-6.
TP	AL	"The Design of Band-Gap Reference Circuits: Trials and Tribulations," IEEE Proceedings of the 1990 Bipolar Circuits and Technology Meeting, September 1990, pp. 1-14.
TP	AM	Jenkins, A., "Analogue and Mixed Signal Integrated Circuit Design," Text & Images Bolton Institute and Northumbria University, 1999, pp. 1-9.
TP	AN	Kauffman, B., "Basic Operation of Bandgap Voltage References," AMI Semiconductor Application Specifics Newsletter, 1998, pp. 1-3.
TP	AO	"Selecting Voltage References," Maxim Integrated Products, 2001, pp. 1-15.

EXAMINER Trong Phan	DATE CONSIDERED 2/3/04

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Form P7-27/V1/1/94   H:\IP\Documents\Clients\Micron Technology\100\S00118.02\S00118.02 pto 1449 2.doc

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